

2/6/04

Patent

Docket No. MTI-31532

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Weimer, Ronald A.  
Serial No. : 09/941,827  
Filing Date : August 29, 2001  
For : Method of Improved High K Dielectric-Polysilicon Interface  
for CMOS Devices  
Confirmation No. : 7551  
Examiner : Yennhu B Huynh  
Group Art Unit : 2813

**CERTIFICATION UNDER 37 CFR 1.8(a) and 1.10**

I hereby certify that, on the date shown below, this correspondence is being:

- ☐ deposited with the United States Postal Service in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. **Mailing**  
37 CFR 1.8(a)  
☐ with sufficient postage as first class mail ☐ As "Express Mail Post Office to Addressee" Mailing Label No. **37 CFR 1.10**  
**Transmission**  
☒ transmitted by facsimile to Fax No.: 703-872-9306 addressed to Examiner Yennhu B Huynh at the Patent and Trademark Office.

Date: February 9, 2004

Jane R. Aoud

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**STATUS INQUIRY**

Dear Sir:

This application was filed on August 29, 2001, and the last correspondence filed in this application is a Response After Non-Final action dated September 4, 2003.

More than **twenty-nine (29)** months (from the filing date) and **five (5)** months (from the date of the Response After Non-Final action) have passed without receiving any communication from the U.S. Patent and Trademark Office. Please provide us with a status of this application.

Respectfully submitted,

Dated: February 6, 2004

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